8593E-10

S	Е	R	V	I	С	Е	Ν	0	Т	
							SUPERSED	ES: None	;	
859	3E Spe	ectrum	Analy	zer						
Seria	als:0000/	A00000 to	o 3513A	999999						
	0000	U00000 to	9999U	J 99999						
Mor	lificatio	on to A1	4 1 00	Ampl	ifior Δc	sev to c	ontrol overs	hoot pr	ohlem	
WOC	incan		4 LUY	Ашрі		55y 10 C	ontroi overs	noot pr	Obieiii	
To B	e Perfor	med By:	Agilent	Person	nel or Q	ualified C	ustomer Person	nel		
		-								
_		rvice Not	es:							
	E-08									
	E-10									
	E-10									
	E-10									
8596	E-10									
Part	s Requir	red:								
Part	No.	Qty.	D	escripti	on					
0360	-0124	1	Te	erminal,	single p	in				
0180	-4136	1	C	apacitor	, tantalui	n 10 ufd.				
9320	-6209	1			590-603					
								(7 antinue d	1

Continued

DATE: January 1996

ADMINISTRATIVE INFORMATION

SERVICE NOTE CLASSIFICATION:										
MODIFICATION RECOMMENDED										
ACTION CATEGORY:	 IMMEDIATELY ON SPECIFIED FAILURE AGREEABLE TIME 	STANDARDS: Labor 1.5 Hour								
LOCATION CATEGORY:	 CUSTOMER INSTALLABLE ON-SITE SERVICE CENTER 	SERVICE RETURN USED RETURN INVENTORY: SCRAP PARTS: SCRAP SEE TEXT SEE TEXT								
AVAILABILITY:	PRODUCT'S SUPPORT LIFE	AGILENT RESPONSIBLE UNTIL: January 1998								
AUTHOR: PS	ENTITY: 5320	ADDITIONAL INFORMATION:								

© 1996 AGILENT TECHNOLOGIES
PRINTED IN U.S.A.



Situation:

An overshoot problem has been observed when users make power vs time measurements on a PHS like signal even without the PHS DLP. The analyzer has this overshoot for fast turn on large dynamic range bursts (50 dB or so). This is generally associated with Digital Communication types of measurements.

Contact Paul Schmiedeberg at (707) 577-2941 or by desk memo and a packet of parts will be supplied for the modification.

Solution/Action:

A 10 ufd. capacitor is added to the +11 volt regulated circuit. The capacitor is actually tied to the +11 V2 output to ground. The effect of the capacitor is to suppress the overshoot that has been experienced as described in the Situation description.

- 1. Lift the top lead of R112 from the circuit board at Point B, see Figure 1.
- 2. Place the terminal stud (p/n 0360-0124) into Point B and solder in place.
- 3. Place the (+) end of the capacitor (C78) into the hole at Point A and solder into place.
- 4. Wrap the (-) lead of C78 and the loose lead of R112 around the terminal stud in Point B and solder.
- 5. Apply one label with the 08590-60373 imprinted to the assembly over the 5062-8261 part number and place one label on the rear panel for quick identification of the modified Log assembly.

After re-assembly, the only calibration necessary is to run the Cal Freq and AmpEd self test from the front panel of the spectrum analyzer.

